

ELECTRONIC CIRCUIT APPARATUS AND INTEGRATED CIRCUIT  
DEVICE

ABSTRACT OF THE DISCLOSURE

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An electronic circuit apparatus and integrated circuit device, wherein an arrangement of connection terminals, external connection terminals and input/output interface circuits of a semiconductor chip as a unit  
10 circuit device is optimized so as to attain suppression of a power consumption and a shorter signal transmission time, configured that only connection pads are allocated to be arranged on a mutually adjacent side of semiconductor chips 1 and 2, and input/output interface  
15 circuits, test pads and external connection pads are arranged along remaining three sides, moreover, the connection pads and the electronic circuits are directly connected not via the input/output interface circuits.